# **LeCroy**

# Serial ATA Test Solutions OPHY-SATA



# **Key Features**

- QPHY-SATA provides a highly automated and easy-to-use solution for Serial ATA Gen1 (1.5 Gb/s), Gen2 (3.0 Gb/s) and Gen3 (6.0 Gb/s) compliance testing
- Bandwidths and sample rates suited to the data and edge rates of today's highest speed devices
- Long memory to capture lowspeed phenomena—such as Spread Spectrum Clocks (SSC) embedded in high-speed data
- Powerful and flexible analysis tools offer unmatched insight for diagnosing and solving elusive problems
- Fast processing throughput maintains interactive operation even while performing complex calculations on long waveforms



The QualiPHY SATA software option for LeCroy oscilloscopes provides an extensive set of validation/verification and debug tools written in accordance with SATA Gen1 (1.5 Gb/s), SATA Gen2 (3.0 Gb/s) and SATA Gen3 (6.0 Gb/s) electrical specification. QPHY-SATA covers the interoperability tests defined by the SATA-IO including all the PHY General Requirements (PHY), PHY Transmitted Signal Requirements (TSG), and PHY OOB Requirements (OOB) for internal SATA products as well as eSATA products. These capabilities make QPHY-SATA an all-inclusive automated test suite that meets the requirements for Gen1 (1.5 Gb/s), Gen2 (3.0 Gb/s) and Gen3 (6.0 Gb/s) Serial ATA transmitter compliance testing.



QPHY-SATA provides a highly automated and easy-to-use solution for Serial ATA Gen1 (1.5 Gb/s), Gen2 (3.0 Gb/s) and Gen3 (6.0 Gb/s) compliance testing in accordance with SATA-IO Logo requirements.

# **Powerful Debugging Tools**

The use of Spread Spectrum Clocking (SSC) in transmitted signals is an optional feature of the SATA specification. When SSC is enabled, measurements such as frequency modulation rate and deviation must also be verified per the specification. The QPHY-SATA software works in conjunction with the deep memory and powerful analysis library of LeCroy oscilloscopes to greatly simplify this verification process. Clock recovery circuitry and clock extraction via Phase-Locked Loops (PLLs) is another significant design consideration. Flexible clock recovery options allow for the replication of virtually any real receiver PLL configuration, enabling the simulation of "what if" scenarios. Jitter results can be measured exactly as the receiver would see them.

# **Comprehensive and Easy-to-read Test Reports**

Measurement results often need to be summarized and tabulated to quickly verify specifications. This information, together with instrument and signal acquisition/test condition setups, results in a fully documented record. QPHY-SATA streamlines this process by incorporating an automatic HTML report generation engine. The created test reports contain tabulated numerical values for each individual test result, including PASS/FAIL and specification limit columns. Reports can also be saved as PDF, HTML or XML.

# **Advanced Debug Capability**

If a compliance failure is found, LeCroy's SDA II serial data analysis package is available to help find the root cause quickly and easily. SDA II has the ability to perform Eye and Jitter



**Spread Spectrum Clock (SSC) validation** requires long memory and specialized analysis functions. LeCroy is the only manufacturer that can precisely track SSC profiles through multiple cycles in real time.



**OOB timing measures** COMINIT/COMRESET and COMWAKE bit rate, burst and gap widths.



**Clock-to-data Jitter Measurements.** Clock recovery options enable you to meet the Jitter Transfer Function (JTF) specification requirements. Industry-leading Q-Scale analysis provides the industry's most precise breakdown of Random and Deterministic Jitter (Rj and Dj).



Connection Diagrams prompt the user to set the DUT to output specific test patterns.

		QualiPH				
SA	TA P	PHY, TSG, OOB	Test Repo	rt		
Ove	rall re	esult: Pass				
DUT: Comment:		Demo	Demo			
		11000007	11000007101700			
Operate	test: or:	11/30/2007 1	11/30/2007 18:17:29			
Temperature		0° C				
Configu	ration in us	Demo of 3.0 (	3b/s tests without SSC			
Limits in	n use:	Gen2i	Gen2i			
Oscillos	ro in use: scope Name	E LCRY0409N1	SATA PHY, TSG, OOB LCRY0409N11978 Model: SDA11000			
Oscillos	scope Seria	LCRY0409N1	LCRY0409N11978 Model: SDA11000			
Computer:		LeCroy Test	LeCroy Test			
Oscillos	scope firmw	are version: 5.2.2.1 (Build	5.2.2.1 (Build 100943)			
QualiPH	HY core ver	sion: 5.2.1.0 (Build	5.2.1.0 (Build 100974)			
Dialips	HY script ve	rsion: 1.1.5	1.1.5			
Stylesh	eet version orm path on	: 1.1 oscilloscope: D:\Waveform	s\SATA			
Stylesh Wavefo	eet version orm path on <b>mary</b> Table]	: 1.1 oscilloscope: D:Waveform 7 Table	SATA			
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Variable Stylesh Wavefo Sum Hide 7 Pass 0 V V 0 0 V V 0 0 V	eet version mm path on mm ary fable <u>Test</u> PHY-01 PHY-01 PHY-01 PHY-02 TSG-01 TSG-01 TSG-01 TSG-01 TSG-01 TSG-01	1.1 oscilloscope: D:Waveform Table Measurement Unit Interval Min Unit Interval Max Unit Interval Mean ftol UH HETP LH HETP DH HETP LH HETP LH METP LM METP LM METP A LBP	Current Value           333.295 ps           333.347 ps           333.318 ps           45.17 ppm           323.3 mV           -328.4 mV           651.7 mV           302.7 mV           -307.3 mV           610.0 mV           299.3 mV	Test Criteria         Informational Only         Informational Only         333.217 ps <= n <= 335.117 ps		

Compliance Reports contain all of the tested values, the specific test limits and screen captures. Compliance Reports can be created as HTML, PDF or XML.

measurements simultaneously and is fully integrated into the oscilloscope application software. In addition, SDA II provides insight into the measured Eye and Jitter parameters making it easier to identify the sources of problems.

### **QualiPHY**

QualiPHY is designed to reduce the time, effort and specialized knowledge needed to perform compliance testing on high-speed serial buses.

- Guides the user through each test setup
- Performs each measurement in accordance with the relevant test procedure
- Compares each measured value with the applicable specification limits
- Fully documents all results
- QualiPHY helps the user perform testing the right way—every time!

# **SPECIFICATIONS AND ORDERING INFORMATION**

### **Specifications**

#### **Real Time Oscilloscope Tests**

PHY General Requirements
Test PHY-01 – Unit Interval
Test PHY-02 – Frequency Long-term Accuracy
Test PHY-03 – Spread Spectrum Modulation Frequency
Test PHY-04 – Modulation Deviation

#### PHY Transmitted Signal Requirements

Test TSG-01 – Differential Output Voltage (all VdiffMin, VdiffMax)
Test TSG-02 – Rise/Fall Times
Test TSG-03 – Differential Skew
Test TSG-04 – AC Common Mode Voltage
Test TSG-05 – Rise/Fall Imbalance
Test TSG-06 – Amplitude Imbalance
Test TSG-09 – Gen1 (1.5 Gb/s) TJ at Connector, fbaud/500
Test TSG-10 – Gen1 (1.5 Gb/s) DJ at Connector, fbaud/500

### **Ordering Information**

Product Description	Product Code	
QualiPHY Enabled SATA Software Option	QPHY-SATA	
SATA 1.5 Gb/s, 3.0 Gb/s and 6.0 Gb/s Compliance Test Fixture Includes: TF-SATA-C Test Fixture Four 2 Inch SSMP to SMA Cables	TF-SATA-C	
Multiwrench Tool		
SATA 1.5 Gb/s, 3.0 Gb/s and 6.0 Gb/s Compliance Test Fixture Measure Kit Includes: TF-SATA-C Test Fixture TF-SATA-C Measurement Fixture Eight 2 Inch SSMP to SMA Cables Two SMA 6 dB Attenuators Two SMA 50 Ω Terminators Two 18 Inch SMA to SMA Cables Multiwrench Tool	TF-SATA-C-KIT	

#### PHY Transmitted Signal Requirements (cont'd)

Test TSG-11 – Gen2 (3.0 Gb/s) TJ at Connector, fbaud/500
Test TSG-12 – Gen2 (3.0 Gb/s) DJ at Connector, fbaud/500
Test TSG-13 – Gen3 Transmit Jitter
Test TSG-14 – Gen3 TX Maximum Differential Voltage
Test TSG-15 – Gen3 TX Minimum Differential Voltage
Test TSG-16 – Gen3 TX AC Common Mode Voltage

#### PHY OOB Requirements

Test OOB-01 – Signal Detection Threshold
Test OOB-02 – UI During OOB (bit rate)
Test OOB-03 – COMINIT and COMWAKE Burst Length
Test OOB-04 – COMINIT Gap Length
Test OOB-05 – COMWAKE Gap Length
Test OOB-06 – COMWAKE Gap Detection
Test OOB-07 – COMINIT Gap Detection



TF-SATA-C Test Fixture Kit provides high-performance signal integrity measurements for Gen1, Gen2 and Gen3.

#### **Compatible Oscilloscopes**

WaveMaster®/SDA/DDA 806Zi or higher bandwidth\* (Gen1 (1.5 Gb/s) only) WaveMaster®/SDA/DDA 813Zi or higher bandwidth\* (Gen1 (1.5 Gb/s), Gen2 (3.0 Gb/s) and Gen3 (6.0 Gb/s)

WavePro®/SDA/DDS 760Zi\* (Gen1 (1.5 Gb/s) only)

SDA 6000A XXL<sup>†</sup> (Gen1 (1.5 Gb/s) only)

SDA 6020<sup>†</sup> (Gen1 (1.5 Gb/s) only) SDA 9000<sup>†</sup> (Gen1 (1.5 Gb/s) only)

SDA 11000<sup>†</sup> (Gen1 (1.5 Gb/s) or Gen2 (3.0 Gb/s)

SDA 13000<sup>†</sup> (Gen1 (1.5 Gb/s), Gen2 (3.0 Gb/s) or Gen3 (6.0 Gb/s)

\*SDA II is required for QPHY-SATA. <sup>†</sup>ASDA software option required.

#### **Recommended Additional Equipment**

SAS <i>Tracer/Trainer</i> 3G 1 Port Analyzer/	SS001APA-X
Exerciser System (Includes CATC platform,	
SAS Tracer 3G 1 Port Module,	
SAS Trainer 3G Traffic Generator Module)	
Sierra M6-2 SAS/SATA Platform	SAS-M006-002-X
2 GB Memory	

#### **Customer Service**

LeCroy oscilloscopes and probes are designed, built, and tested to ensure high reliability. In the unlikely event you experience difficulties, our digital oscilloscopes are fully warranted for three years and our probes are warranted for one year.

This warranty includes: • No charge for return shipping

• Long-term 7-year support • Upgrade to latest software at no charge

# **Croy** 1-800-5-LeCroy **Local sales offices are located throughout the world. Visit our website to find the most convenient location.**

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